

**BACKPLANE ARCHITECTURE FOR USE IN
WIRELESS AND WIRELINE ACCESS SYSTEMS****ABSTRACT OF THE DISCLOSURE**

There is disclosed an improved backplane architecture for devices such as processors and modems used in wireless, cable, and wired voice frequency (VF) access systems. The present invention comprises a backplane comprising (1) a low tier that is capable of aggregate traffic rates of up to approximately two gigabits per second, and (2) a high tier that is capable of aggregate traffic rates of up to approximately twenty gigabits per second. The backplane of the present invention also comprises a plurality of bus structures and clock and framing resources. The backplane of the present invention is capable of being used in more than one type of electronic equipment.